## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yojiro MATSUEDA et al.

Application No.:

10/676,072

Filed: October 2, 2003

Group Art Unit: 2871

Docket No.: 117391

For:

CIRCUIT SUBSTRATE, MANUFACTURING METHOD THEREOF, ELECTRO-

OPTICAL DEVICE, AND ELECTRONIC APPARATUS

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- $\boxtimes$ This Information Disclosure Statement is being filed (a) within three months of 1. the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- $\boxtimes$ One or more reference cited herein was cited in a counterpart foreign application. See Reference 1.
- $\boxtimes$ An English language Abstract of one or more non-English language reference is attached hereto. See Reference 1.

 $\boxtimes$ A computer-generated English language translation of one or more Japanese Patent Publication cited herein has been obtained from the website of the Japanese Patent Office ([http://www.jpo.go.jp]), and is attached, but has not been reviewed for accuracy. See Reference 1.

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Date: February 16, 2006

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INFORMATION DISCLOSURE STATEMENT										
(Use several sheets if necessary)				APPLICANTS Yojiro MATSUEDA et al.						
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